IEEE Computer Society COMPSAC2020

Panel, July 14: "To Patent or Not to Patent?"



Hironori Kasahara, Ph.D., IEEE Fellow, IPSJ Fellow Senior Executive Vice President, Waseda University IEEE Computer Society President 2018 URL: http://www.kasahara.cs.waseda.ac.jp/

1980 BS, 82 MS, 85 Ph.D., Dept. EE, Waseda Univ.

1985 Visiting Scholar: U. of California, Berkeley, 1986 Assistant Prof, 1988 Associate Prof, Waseda

1989-90 Research Scholar: U. of Illinois, Urbana-Champaign, Center for Supercomputing R&D,

1997 Prof., 2004 Director, Advanced Multicore Research Institute, Waseda

2017member: the Engineering Academy of Japan (2020 Board), Science Council of Japan

2018 IEEE Computer Society President, Senior Vice President for Reseach & IT, Waseda Univ.

<Committees in Societies and Government: 263>

IEEE Computer Society: President 2018, Executive Committee(2017-2019), BoG(2009-14),

Strategic Planning Committee Chair 2018, Multicore STC Chair (2012-), Japan Chair (2005-07),

IPSJ Chair: HG for Magazine. & J. Edit, Sig. on ARC.

[METI/NEDO] Project Leaders: Multicore for Consumer Electronics, Advanced Parallelizing

Compiler, and **Green Computing**, Chair: Computer Strategy Committee

[Cabinet Office] CSTP Supercomputer Strategic ICT PT, Japan Prize Selection Committees, etc.

[MEXT] Info. Sci. & Tech. Committee, Supercomputers (Earth Simulator, HPCI Promo., Next Gen.

Supercomputer K) Committees, JST Moonshot Project G3 Robot & Al Vice Chair,

[COCN] Board Member in Council of Competitiveness Nippon, etc.



<Research Accomplishment>

Reviewed Papers: 221, Invited Talks: 190,

Granted Patents: 54 (Japan, US, GB, China),

Articles in News Papers, Web News, TV etc.: 625

<AWARD>

1987 IFAC World Congress Young Author Prize 1997 IPSJ Sakai Special Research Award,

2005 STARC Academia-Industry Research Award,

2008 LSI of the Year Second Prize,

2008 Intel Asia Academic Forum Best Research Award,

2010 IEEE CS Golden Core Member Award

2014 Minister of Edu., Sci. & Tech. Research Prize

2015 IPSJ Fellow, 2017 IEEE Fellow, Eta Kappa Nu

2019 Sprit of IEEEComputer Society Award,

2020 IPSJ Contribution Award

Patents related with OSCAR Parallelizing Compiler & Hardware Currently 54 international patents in USA, UK, China, Japan to improve effective performance, cost-performance and software productivity and reduce power

Multigrain Parallelization for Embedded Multicores and Heterogeneous Multicores

& <u>Hardware Supports (Synchronization)</u> coarse-grain parallelism among loops, subroutines & basic blocks among statements in addition to loop parallelism

Data Localization:

Global Cache & Local Memory Optimization

Automatic data management for distributed shared memory, cache and local memory

Software Cache Coherent Control

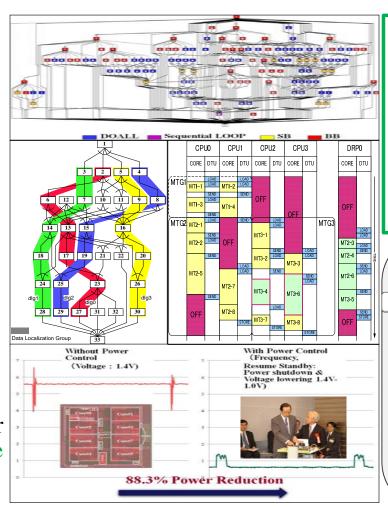
Data Transfer Controller

Data transfer overlapping using <u>Data Transfer</u> <u>Controllers (DMAs) hardware & its compiler</u>

Power Reduction Compiler and Hardware

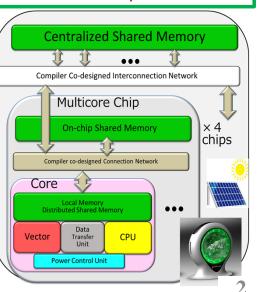
Reduction of consumed power by compiler control DVFS and Power gating with hardware supports.

Accelerator See right figure



Accelerator

can be attached to any processor cores, RISCV, arm, Infineon Renesas, etc, without instruction extensions. It workes with local memory management, DMA & power reduction with full compiler vectorization & parallelization



OSCAR Technology Corp.

Started up on Feb. 28, 2013: New CEO from April 1, 2020

Though the all patents and OSCAR compiler from Waseda Univ. have been licensed until March 31,

"all Hardware" and "Software Patents outside USA and Japan" can be licensed to any companies since April 1, 2020

New CEO from April 1, 2020: Mr. Satoshi Miki (CEO. Fixstars)

Previous Executives

Dr. M. Ohashi: COO (Ex- OO of Ono Sokki)

Mr. A. Nodomi : CTO (Ex- Spansion)

Dr. K. Shirai (Ex- President of Waseda U., Ex- Chairman of Japanese Open U.)

Mr. S. Tsuchida (Co-Chief Investment Officer of Innovation Network Corp. of Japan)

Auditor: Mr. S. Honda (Ex- Senior VP and General Manager of MUFG)

Dr. S. Matuda (Emeritus Prof. of Waseda U., Chairman of WERU INVESTMENT)

Mr. Y. Hirowatari (President of AGS Consulting)

Advisors: Prof. H. Kasahara (Waseda U.)

Prof. K. Kimura (Waseda U.)

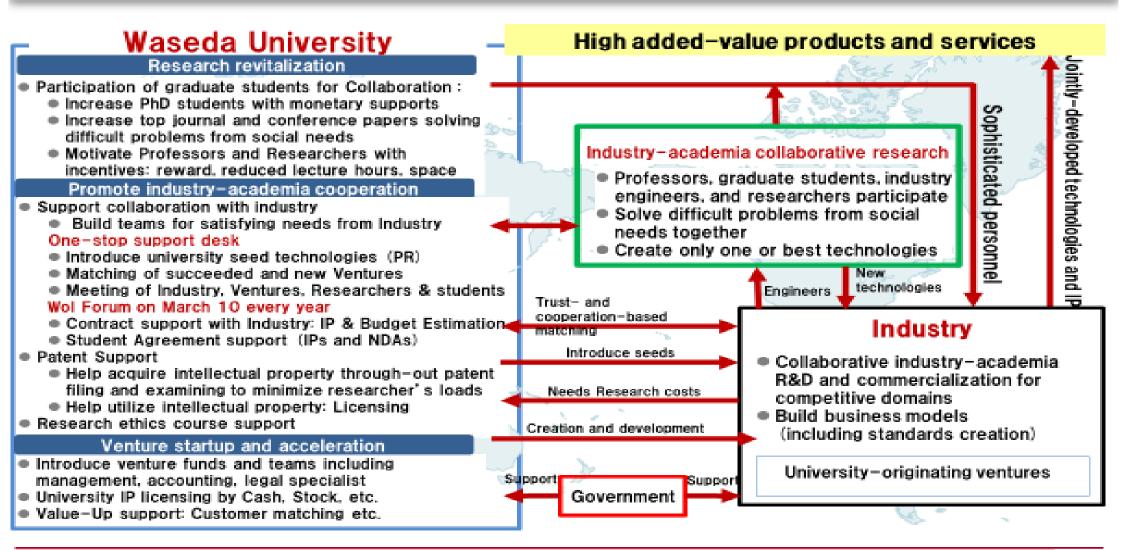
Dr. T. Ono (Ex- CEO of OSCARTECH & First Section-listed Company,

Director of National U., Invited Prof. of Waseda U.)

Mr. K. Ashida (Ex- VP Sumitomo Trading, Adhida Consult. CEQ)



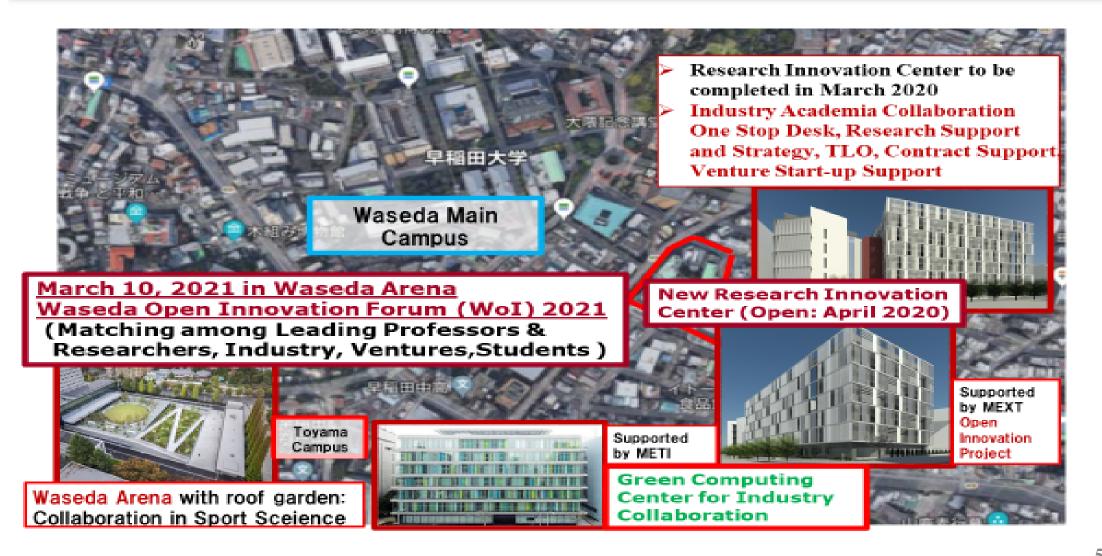
Waseda University Open Innovation Ecosystem





Waseda University

Waseda Open Innovation Valley Project



Number of **International Students** 7,942 125 countries and territories **ALUMNI** [卒業生] **Graduate Employability**

Alumni CEOs in Japan

10,606

8 Prime **Ministers**

Founder Shigenobu OKUMA



(#2 in Japan, #27 in the world)

630,000

PARTNER **INSTITUTIONS** [協定大学・機関]

848 (93 countries)









The University Professor Waseda, Waseda Alumnus, Prof. Emeritus Nagoya Univ., Prof. Meijo Univ. IEEE President 2020. The first from Asia in 135 years history. IEEE has 420,000 members.



Tadashi YANAI



Business

Leaders

CEOs of global companies

Shiseido

Nomura Securities Co., Ltd.

Tokio Marine & Nichido Fire Insurance Co., Ltd.



Haruki **MURAKAMI**



Hirokazu **KOREEDA**





Daiya SETO

in private university of Japan

FACULTY

ENROLLMENT [学生数]

UNDERGRADUATE **STUDENTS** [学部生]

41,051

GRADUATE STUDENTS

[大学院生]

8,385



49,436 5,468

NUMBER OF BOOKS [図書館蔵書]

5,800,000



President International Political Science Association (IPSA) President 2016



Senior Executive Vice President **IEEE Computer Society President** 2018. The first president from outside USA and Canada in 72 years CS history. CS has 84,000 members from, 168 countries.



Prime **Ministers**

- 8th Shigenobu Okuma 17th Shigenobu Okuma
- 55th Tanzan Ishibashi
- 74th Noboru Takeshita
- 76th Toshiki Kaifu 84th Keizo Obuchi
- 85th Yoshiro Mori
- 91st Yasuo Fukuda
- 95th Yoshihiko Noda

早稲田大学





Yuzuru HANYU

