

# IEEE COOL Chips21, April 18-20, 2018

## Symposium on Low-Power and High-Speed Chips and Systems



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**IEEE Computer Society President 2018**

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**URL: <http://www.kasahara.cs.waseda.ac.jp/>**

**1980 BS, 82 MS, 85 Ph.D. , Dept. EE, Waseda Univ.**  
**1985 Visiting Scholar: U. of California, Berkeley**  
**1986 Assistant Prof., 1988 Associate Prof., 1997,**  
**Waseda Univ., Now Dept. of Computer Sci. & Eng.**  
**1989-90 Research Scholar: U. of Illinois, Urbana-**  
**Champaign, Center for Supercomputing R&D**  
**2004 Director, Advanced Multicore Research**  
**Institute, 2017 member: the Engineering Academy**  
**of Japan and the Science Council of Japan**

**2005 STARC Academia-Industry Research Award**  
**2008 LSI of the Year Second Prize**  
**2008 Intel AsiaAcademic Forum Best Research Award**  
**2010 IEEE CS Golden Core Member Award**  
**2014 Minister of Edu., Sci. & Tech. Research Prize**  
**2015 IPSJ Fellow**  
**2017 IEEE Fellow, IEEE Eta Kappa Nu**











**Reviewed Papers: 216, Invited Talks: 155, Published**  
**Unexamined Patent Application:66 (Japan, US, GB,**  
**China Granted Patents: 42), Articles in News Papers,**  
**Web News, Medias incl. TV etc.: 578**

**Committees in Societies and Government 255**

**IEEE Computer Society President 2018, BoG(2009-**  
**14), Multicore STC Chair (2012-), Japan Chair (2005-**  
**07), IPSJ Chair: HG for Mag. & J. Edit, Sig. on ARC.**  
**【METI/NEDO】 Project Leaders: Multicore for**  
**Consumer Electronics, Advanced Parallelizing**  
**Compiler, Chair: Computer Strategy Committee**  
**【Cabinet Office】 CSTP Supercomputer Strategic**  
**ICT PT, Japan Prize Selection Committees, etc.**  
**【MEXT】 Info. Sci. & Tech. Committee,**  
**Supercomputers (Earth Simulator, HPCI Promo.,**  
**Next Gen. Supercomputer K) Committees, etc.**

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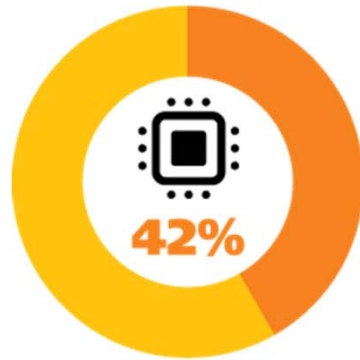
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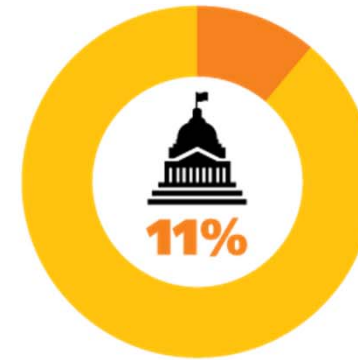
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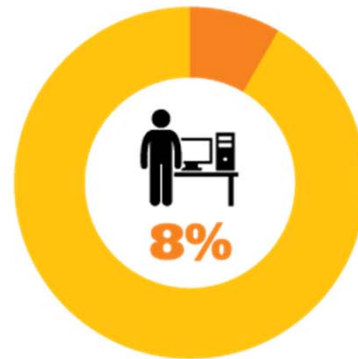
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# IEEE Computer Society BoG (Board of Governors) Feb.1, 2018



14-Mar-18

<https://www.computer.org/web/cshistory/officers-2018>



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<https://ieeecompsac.computer.org/2018/>



**Bjarne Stroustrup**  
2018 Computer  
Society Computer  
Pioneer Award  
Columbia University

5

14-Mar-18



**Masaru Kitsuregawa**  
Director General of  
NII, Past President of  
IPSJ



**Margaret Martonosi**  
2018 Computer  
Society Technical  
Achievement  
Princeton University



**Dejan Milojicic**  
CS President 2014  
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- **Dynamic Parallelization: Rudolf Eigenmann**
- **Instruction Level Parallelization: Alexandru Nicolau**
- **Multigrain Parallelization and Power Reduction:  
Hironori Kasahara**
- **The Polyhedral Model: Paul Feautrier**
- **Vector Computation: David Kuck (Computer Pioneer)**
- **Vectorization: P. Sadayappan**
- **Vectorization/Parallelization in the IBM Compiler:  
Yaoqing Gao**
- **Vectorization/Parallelization in the Intel Compiler:  
Peng Tu**
- **Roundtable Discussion by all presenters**

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- CEU: 1.2

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For questions, please contact  
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# Toward 2018

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CS Point (Mileage) System: Annual & Life Time Honor, Premier Seating, Premier Registration, Distinguished Reviewer, etc
3. Offering more attractive services for practitioners in industry;
4. Providing the world's best educational content and historical treasures for future generations, which only the CS can create with our pioneering researchers (for example, the Multicore Compiler Video Series found at [www.computer.org/web/education/multicore-video-series](http://www.computer.org/web/education/multicore-video-series));
5. Thinking about sustainable membership fees while considering the diversity of economic situations within the 10 regions;
6. Cooperating with other IEEE societies and sister societies in a timely and efficient manner;
7. Intelligibly introducing the latest computer-related technologies to younger generations, including children, so that they can realize their technological dreams.