



Hironori Kasahara

IEEE Computer Society President Candidate

Election: Aug. 3 - 12:00 noon EDT Oct.5, 2015

Voting: [http:// www.computer.org/election2015](http://www.computer.org/election2015)

Position statement

The Computer Society (CS) serves a significant role in realizing a fulfilling life through the latest technologies such as smartphones, the Internet of Things, 3D printers, smart cities, self-driving automobiles, heavy particle beam cancer therapy systems, big data analysis, cloud servers, and supercomputers.

The CS's activities such as Education, Conferences, Publishing, Member Services, and Standards, as well as passing down pioneering work to future generations, are supported by volunteers. From my 34 years of membership and volunteer service, I know how important it is to thank explicitly those volunteers who contribute so much to the CS. Motivating increased participation in the above activities benefits academia, industry, governments, and ordinary people globally.

If elected, I will do my best to realize following plans.

1. Rewards system to thank the volunteers explicitly

I will start a design of a system to thank the volunteers to improve their motivation to undertake activities that benefit humanity. I am thinking of introducing a kind of rewards system. In this system, the volunteers can accumulate points from their work as a chair or a member of committees for Governance, Conferences, Publications, Education, Standards, History, as well as reviewers for CS publications, taking into account promptness and quality. We may have honors for activities performed every year and over a lifetime—for example, naming premiere members, distinguished reviewers, lifetime VIP members, and so on. The concrete implementation should be discussed in a committee.

2. Closer relationship with industry

Also, I would like to come up with a way to develop a closer relationship with industry. Decreasing of industrial members has been a big problem for every academic society. Based on my 30-year experience with industry and academia collaborations, including startup offerings with my patents and software. Through this activity, the CS will be able to contribute to matching needs from industry and seeds from academia and promptly supplying our latest technologies as value-added products to ordinary people for more convenient life.

3. Top researchers' videos for catching up the latest technologies and as presents for future generations

Furthermore, I will continue to create new activities to promote research and development and to hand down technical treasures to future generations. As an example of the activities, our Multicore STC already developed the Multicore Compiler Video Course with top researchers representing this era.

Biography

Hironori Kasahara has served as chair or member of 220 society and governmental committees, including being member of IEEE CS Board of Governors, Constitution & Bylaws, and Nomination Committees; Chair of CS Multicore STC; Associate Editor of CS *Transactions on Computers*; member of IEEE Ad Hoc on Serving Individuals in Industry; Board of IEEE Tokyo Section; MEXT Earth Simulator and K supercomputer committees; and chair of the IEEE CS Japan Chapter and IPSJ SIG on Computer Architecture.

He received a Ph.D. in electrical engineering from Waseda University, Tokyo, in 1985. In Waseda University, he was an assistant professor in 1986, an associate professor in 1988 and has been a professor of computer science and Engineering since 1997.

Also, he has been a director of the Advanced Multicore Research Institute, Waseda University since 2004. He was a visiting scholar at the University of California at Berkeley in 1985 and the University of Illinois at Urbana-Champaign's Center for Supercomputing R&D in 1989-1990.

He received the CS Golden Core Member Award, IFAC World Congress Young Author Prize, IPSJ Fellow and Sakai Special Research Award, and Science and Technology Prize from Japanese Minister of Education, Science, and Technology.

He led Japanese METI/NEDO national projects on parallelizing compilers, embedded multicores, and green computing. His work has been presented in 207 papers, 127 invited talks, 27 patents, and 490 newspaper and Web articles.

(The opinions on this page are mine and are not necessarily those of the IEEE Computer Society or the IEEE.)