

The **25th** International Workshop on Languages and Compilers for Parallel Computing

Sep 11-13, 2012

Organizing Committee Optional Tour Location

25th Anniversary LCPC workshop

ノグ・システム研究開発センタ





The LCPC workshop is a forum for sharing cutting-edge research on all aspects of parallel languages, compilers and related topics including runtime systems and tools. The scope of the workshop spans foundational results and practical experience, and all classes of parallel processors including concurrent, multithreaded, multicore, accelerated, multiprocessor, and tightly-clustered systems. Given the rise of multicore processors, LCPC is particularly interested in work that seeks to transition parallel programming into the computing mainstream.

Specific topics of interest include (but are not limited to):

- Parallel programming models
- Parallel programming languages
- Compiling for parallelism
- Automatic parallelization
- Optimization of parallel programs
- Formal analysis and verification of parallel programs
- Parallel runtime systems
- Task-parallel libraries
- Parallel application frameworks
- Performance analysis tools
- Debugging tools for parallel programs
- Parallel algorithms
- Parallel applications
- Synchronization and concurrency control
- Software engineering for parallel programs
- Fault tolerance for parallel systems
- Adaptive compilation and optimization of parallel programs
- Software techniques for accelerators (including GPGPUs)
- Compilers for Multicores and Manycores
- Power Optimization by Compilers
- APIs for Homogeneous and Heterogeneous multicores
- Compilers and APIs for Accelerators

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IMPORTANT DATE

Abstracts: May 29, 2012

(midnight pacific time) (no auto

Full papers: June 5, 2012

(midnight pacific time) (no automatic

Notification: July 23, 2012

Hotel Reservation for the Workshop Rate: Aug. 11, 2012

Final Papers (Electric version): Aug.

SERVER POWER CONSUMPTION



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embedded multicore RPX.