#### **IEEE COMPSAC2021 CS Presidents Panel**



Hironori Kasahara, Ph.D., IEEE Fellow, IPSJ Fellow Senior Executive Vice President, Waseda University IEEE Computer Society President 2018 URL: http://www.kasahara.cs.waseda.ac.jp/



1980 BS, 82 MS, 85 Ph.D., Dept. EE, Waseda Univ.
1985 Visiting Scholar: U. of California, Berkeley,
1986 Assistant Prof., 1988 Associate Prof., 1989-90
Research Scholar: U. of Illinois, Urbana-Champaign,
Center for Supercomputing R&D, 1997 Prof.,
2004 Director, Advanced Multicore Research Institute,
2017member: the Engineering Academy of Japan (2020
Board Mem) and the Science Council of Japan
2018 IEEE Computer Society President
Nov. Senior Vice President, Waseda Univ.

AWARD: 1987 IFAC World Congress Young Author Prize
1997 IPSJ Sakai Special Research Award,
2005 STARC Academia-Industry Research Award,
2008 LSI of the Year Second Prize,
2008 Intel Asia Academic Forum Best Research Award,
2010 IEEE CS Golden Core Member Award
2014 Minister of Edu., Sci. & Tech. Research Prize
2015 IPSJ Fellow, 2017 IEEE Fellow, Eta Kappa Nu
2019 Spirit of IEEE Computer Society Award,
2020 IPSJ Contribution Award,

Reviewed Papers: 223, Invited Talks: 215, Granted Patents: 62 (Japan, US, GB, China), Articles in News Papers, Web News, TV etc.: 660

**Committees in Societies and Government 263** 

**IEEE Computer Society: President 2018, Executive** Committee(2017-2019), BoG(2009-14), Strategic Planning Committee Chair 2018, Multicore STC Chair (2012-), Japan Chair(2005-07), IPSJ Chair: HG for Magazine. & J. Edit, Sig. on ARC. [METI/NEDO] Project Leaders: Multicore for **Consumer Electronics, Advanced Parallelizing Compiler, Chair: Computer Strategy Committee Cabinet Office CSTP Supercomputer Strategic ICT** PT, Japan Prize Selection Committees, etc. (MEXT) Info. Sci. & Tech. Committee, Supercomputers (Earth Simulator, HPCI Promo., Next Gen. Supercomputer K) Committees JST Moonshot Project G3 Robot & AI Vice Chair, **[COCN]** Board Member in Council of Competitiveness Nippon, etc.

- How and why you became involved with the CS
- Your experience as a CS volunteer
- •How that contributed to your career, the CS, and the profession
- •Why should someone become a CS volunteer

#### Some of papers in and just after Ph.D. Course in Waseda U.

IEEE TRANSACTIONS ON COMPUTERS, VOL. C-33, NO. 11, NOVEMBER 1984

1023

# Practical Multiprocessor Scheduling Algorithms for Efficient Parallel Processing

HIRONORI KASAHARA, MEMBER, IEEE, AND SEINOSUKE NARITA, SENIOR MEMBER, IEEE



Courtesy of dexchao - Fotolia.com

104

IEEE JOURNAL OF ROBOTICS AND AUTOMATION, VOL. RA-1, NO. 2, JUNE 1985

#### Parallel Processing of Robot-Arm Control Computation on a Multimicroprocessor System

HIRONORI KASAHARA MEMBER, IEEE, AND SEINOSUKE NARITA, SENIOR MEMBER, IEEE

A PARALLEL PROCESSING SCHEME FOR THE SOLUTION OF SPARSE LINEAR EQUATIONS USING STATIC OPTIMAL-MULTIPROCESSOR-SCHEDULING ALGORITHMS

H.Kasahara\*, T.Fujii\*, H.Nakayama\*, S.Narita\*, and Leon O.Chua\*\*

Dept. of Electrical Eng., Waseda University, Tokyo, 160, Japan

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## PARALLEL PROCESSING OF ROBOT MOTION SIMULATION

H. Kasahara, H. Fujii and M. Iwata

Department of Electrical Engineering, Waseda University, 3-4-1 Ohkubo Shinjuku-ku, Tokyo 160, Japan



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#### Practical Innovation

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for this video series to examine the innovative techniques they use to improve reliability and performance while reducing costs, time, and power consumption.

Hear about some of the most advanced powerreduction, parallelization, and vectorization technologies used in a range of industry applications, including automobiles, big data, cloud computing, cluster computing, medical image processing, multimedia, smartphones, and supercomputing.

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Instruction Level Parallelization Alexandru Nicoleu



The Polyhedral Model Paul Feautrier



Vectorization P. Sedayappan



Vectorization/Parallelization in the Intel Compiler Peng Tu



Autoparallelization for GPUs Wen-mei Hwu



Dynamic Parallelization Rudolf Eigenmann



Multigrain Parallelization and Power Reduction Hironori Kasahara



Vector Computation David Kuck



Vectorization/Parallelization in the IBM Compiler Yanging Gao



Roundtable Discussion

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Professionals in any industry that demands real-time processing, high performance, and speed will find these videos an important tool for getting better results from their multicore processing systems and future-proofing their applications.

Educators and graduate students will also find inspiration from this window into the minds of some of the most accomplished experts in multicore.

www.computer.org/multicore-video





#### Prof. David J Kuck (Univ. Illinois, Intel)

**IEEE Computer Pioneer Award 2011** 

Ms. Diane B. Greene (VMware Cofounder & CEO)
IEEE Computer Society Computer Entrepreneur Award 2011





2018.03.08 WasedaUniv. Symposium on Future of High Performance Green Computing 2018 (HPGC2018)



## **IEEE Computer Society**

The first President from the outside of USA and Canada in 72 years history of IEEE CS

Bjarne Stroustrup: Morgan Stanley & Columbia Univ. 2018 IEEE Computer Society Computer Pioneer Award **IEEE COMPSAC2018 Keynote & Award Ceremony** 



May, Geneva



July 25, 2018 Award Ceremony Rihga Royal Hotel Tokyo

IEEE-Eta Kappa Nu





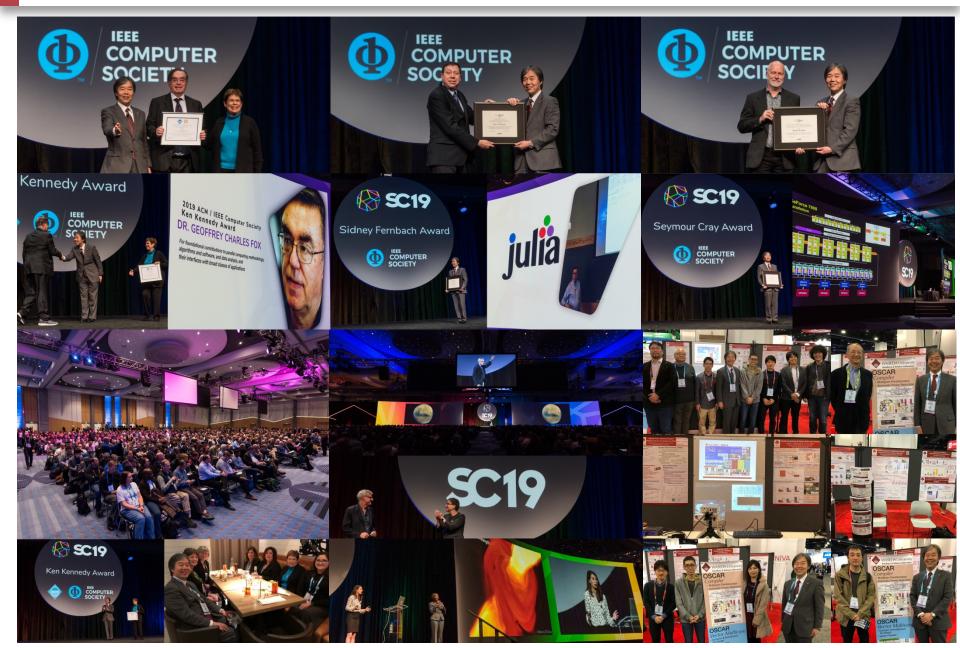
480 chapters

168 countries

**PIEEE** 

31 technical committees & councils

## ACM/IEEE SC (SuperComputing) 19, Denver, Nov.17-22, 2019



#### Cooperation with International Organizations in 2018



IPSJ Leaders, March, IPSJ Convention, Tokyo



Japan (IPSJ), China(CCF), Korea(KIISE) in March, Waseda U., Tokyo



Okawa Foundation, CS Japan Chapter, Multicore STC & Japanese Government Symp.



MoU with UN ITU in AI for Good, May, Geneva



CCF China National Computer Congress, Oct., Hangzhou



MoU with Baidu, July, Green Comp. C., Tokyo



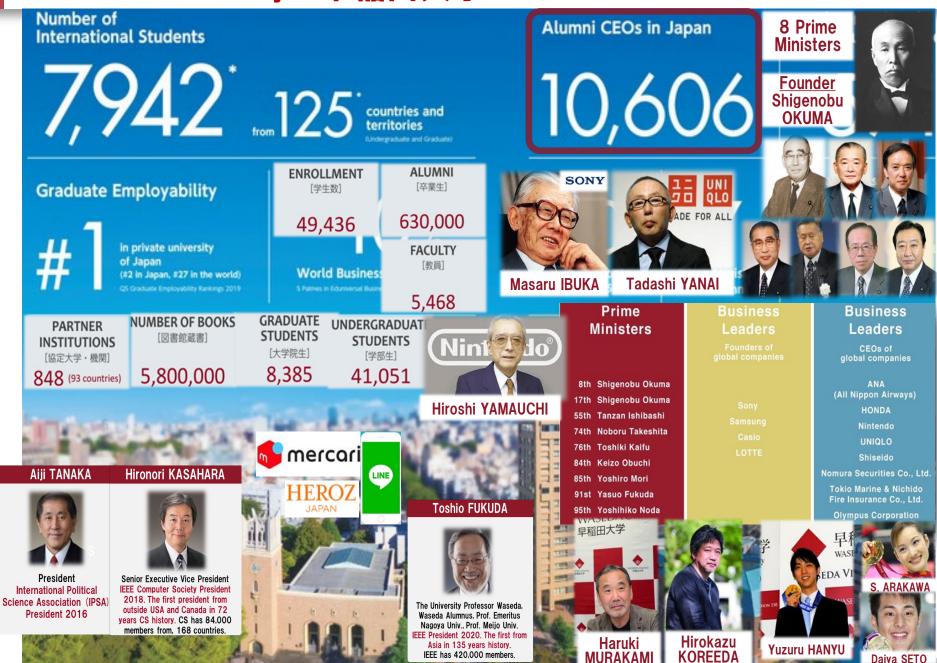
Russian Academy of Science: Russian Computer Science 70th Anniversary, Nov., Moscow



IEEE CS China Office moderated Tencent-Waseda Univ. Joint Symposium, Nov., Waseda U., Tokyo

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### WASEDA University -早稲田大学-



#### THE World Academic Summit, ETH, 2019.9.10



#### **World Leading University Presidents Panel**



## Oxford University, 11/12-13,2019(CSでの招待講演及び連携協議)

#### **Vice Chancellor Prof. Louise Richardson** (WOI 2020で基調講演)

Head of Astrophysics: Prof. Rob Fender Dept. of Physics: Prof. Ian Shipsey Astrophysics: Prof. H.Falche, et. al.

**Merton College** 

Warden: Prof. Irene Tracy (2020年1月に来学)

Fellow: Dr. Peter Braam

**Sub Warden: Prof. Judy Armitage** 

**CS: Prof. Jeremy Gibbons** 



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# 1987 OSCAR(Optimally Scheduled Advanced Multiprocessor) Co-design of Compiler and Architecture

Looking at various applications, design a parallelizing compiler and design a multiprocessor/multicore-processor to support compiler optimization Easy to Use, Collaboration of Software and Hardware, Useful for World People



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MoU with UN ITU
in AI for Good,
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July 25, 2018 Award Ceremony Rihga Royal Hotel Tokyo

IEEE-Eta Kappa Nu





480 chapters

168 countries

**PIEEE** 

31 technical committees& councils



#### Toward Realization of Waseda Open Innovation Ecosystem

International Collaboration

Industry-Academia Collaboration

**Venture Startup & Acceleration** 

Development of Human Resource Understanding Social Needs

Social Sciences, Humanities, Science & Engineering Cooperation

Research Promotion (Including Patents Creation & Licensing)

Matching among people in Industry, Government, Academia, and Waseda

