

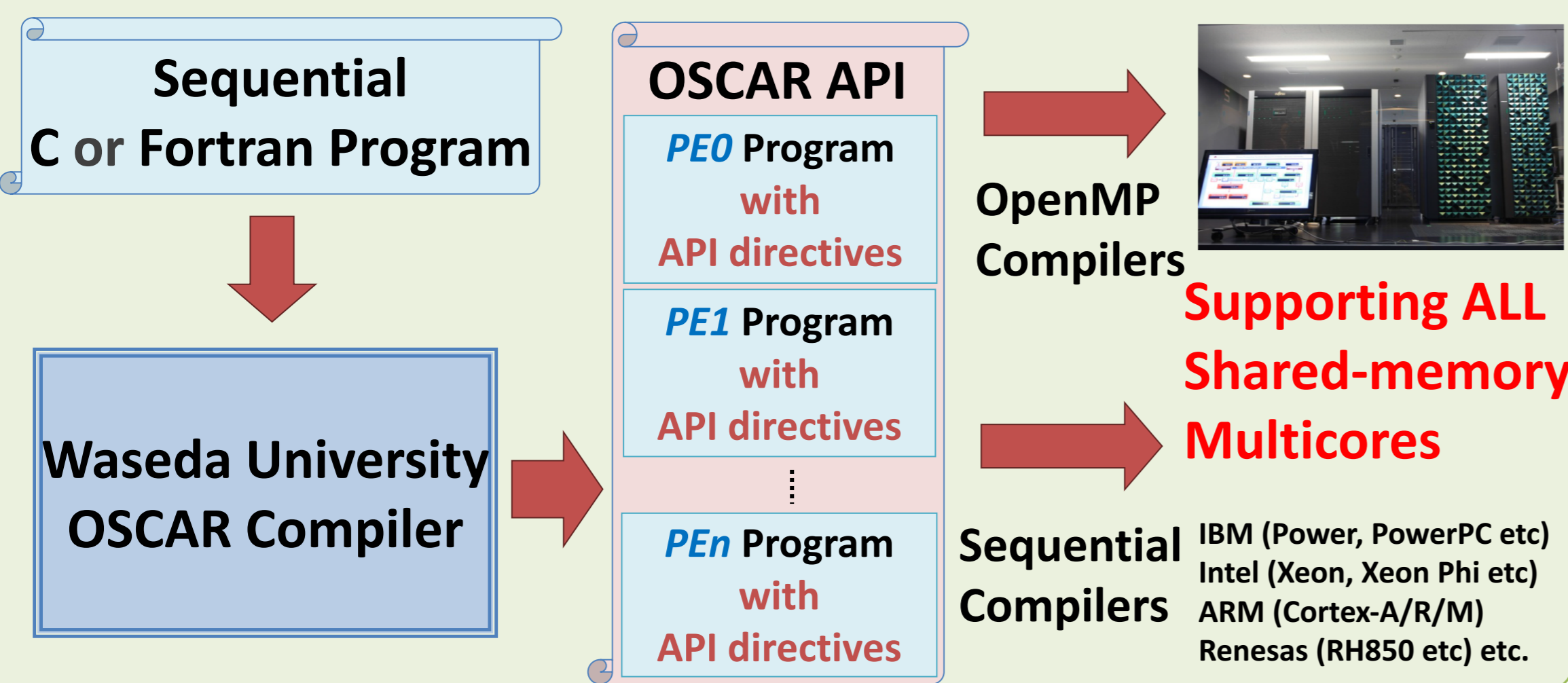
OSCAR Parallelizing & Power Reducing Compiler

Automatic Parallelized and **Automatic Power Reduced**
 Software for Embedded to HPC Programs on Multicores

<Application>

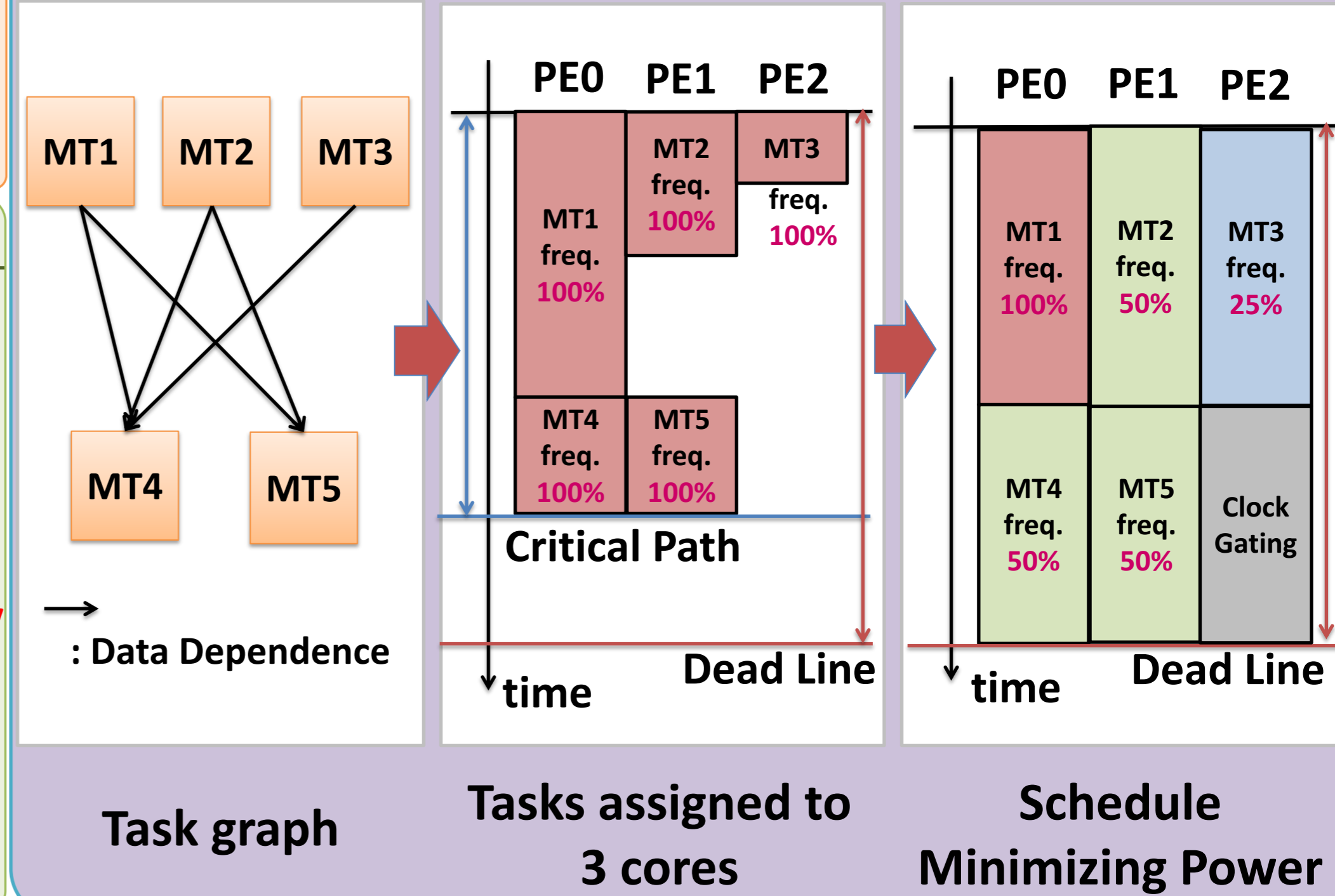
Automobile, Medical, Image Processing, Scientific HPC

Multi-Platform Support (IBM, Intel, ARM, Renesas, etc.)



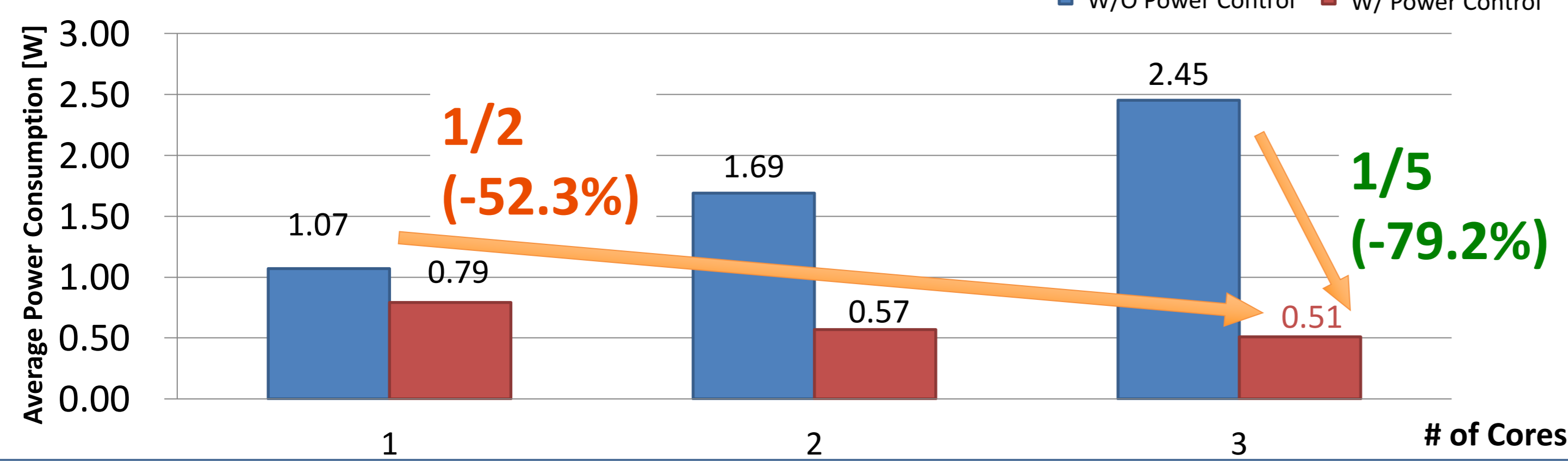
Saving Energy by Compiler

Parallelization → Power Reduction

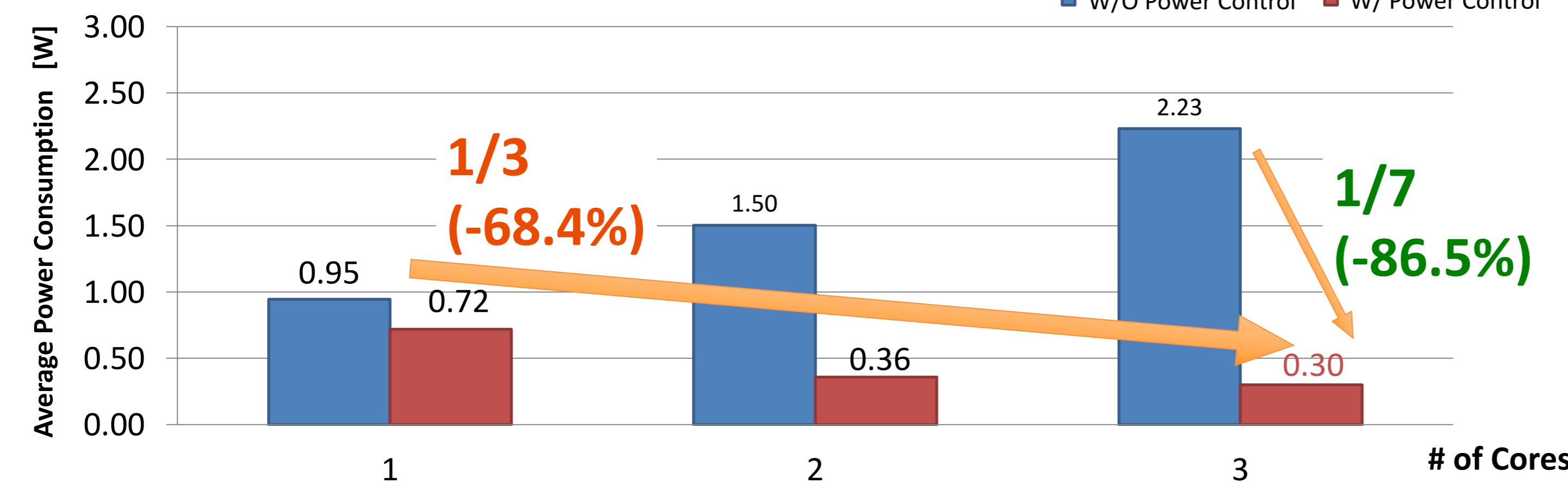


Power reduction on ARM Cortex-A

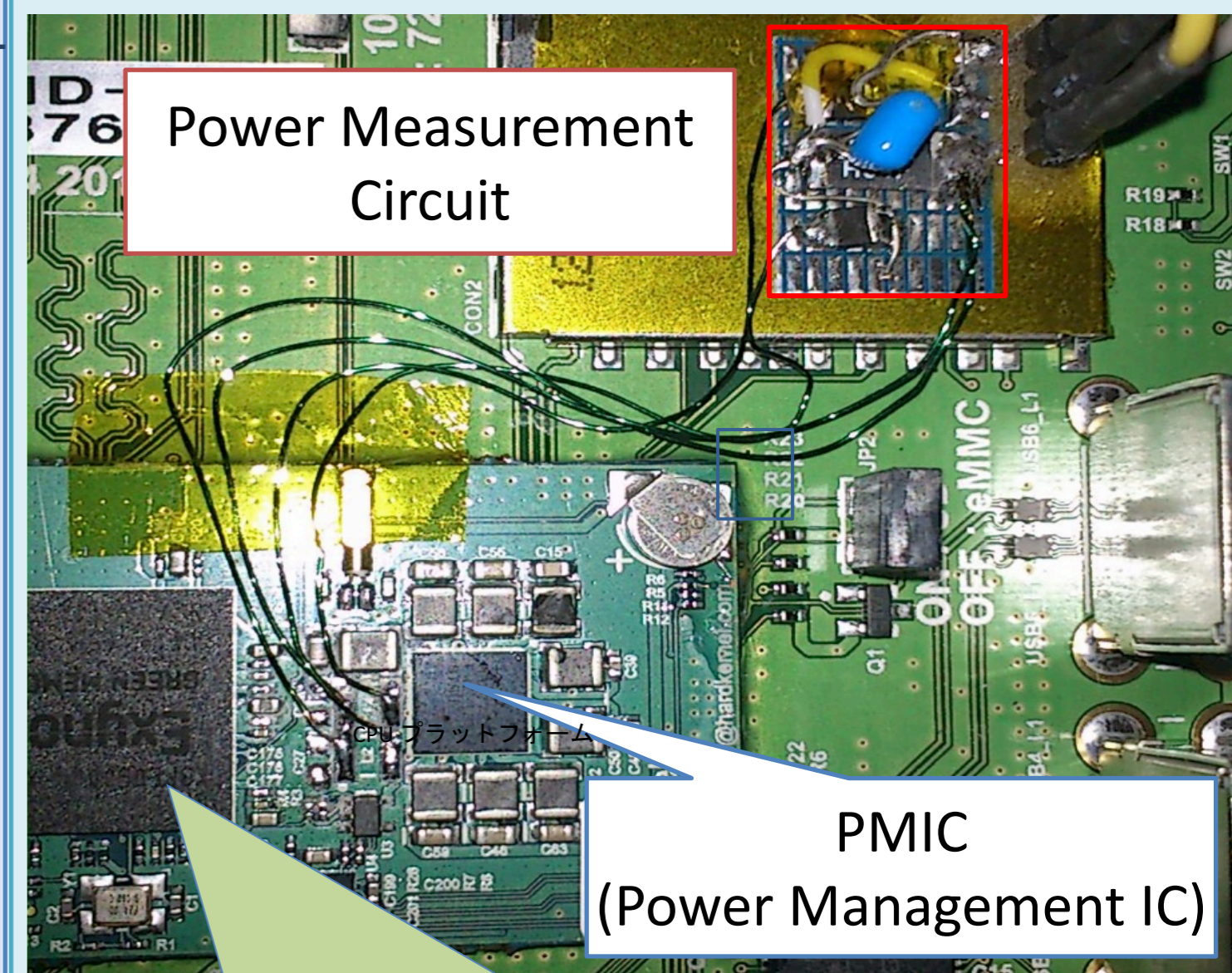
Power Consumption of H.264 Decoder



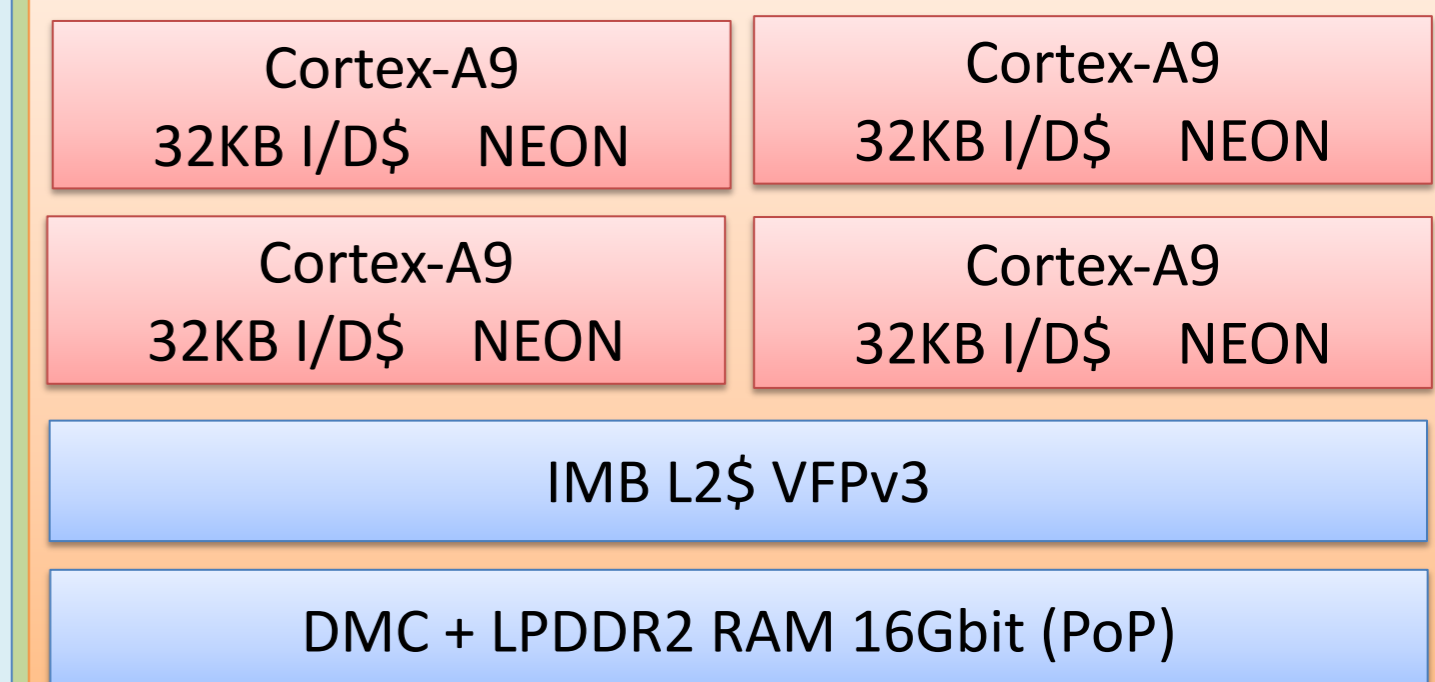
Power Consumption of Optical Flow



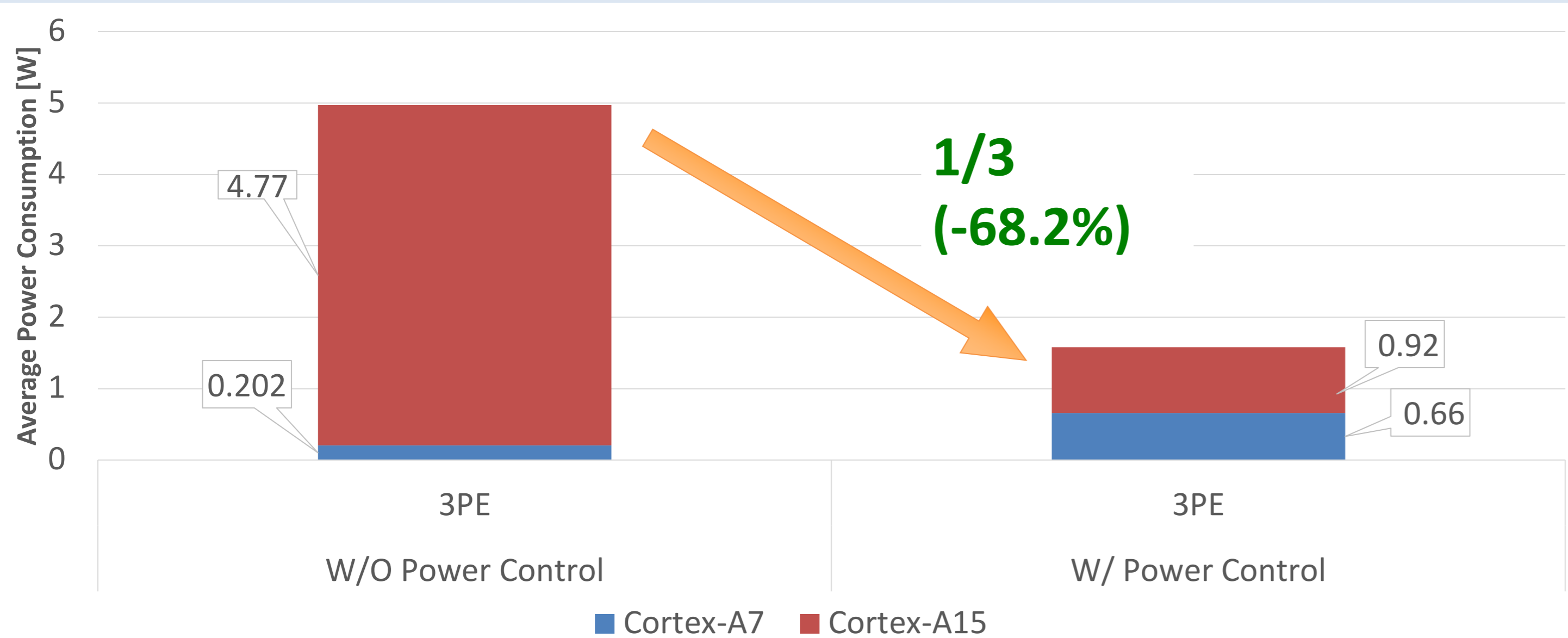
Evaluation Environment : ODRROID-X2 (ARM 4 cores)



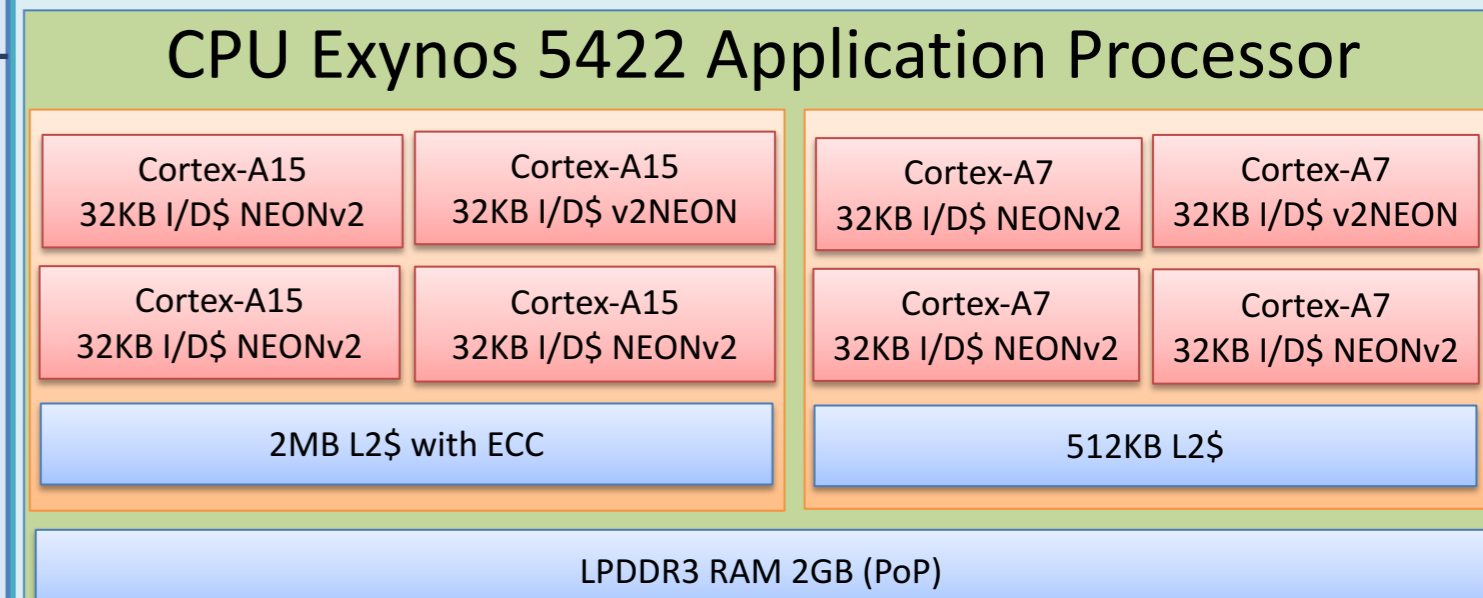
CPU Exynos 4412 Application Processor



Power reduction on ARM big.LITTLE



Evaluation Environment : ODRROID-XU3 (ARM big.LITTLE 4 cores)



OpenCV Facedetect (using haar Cascades)

