2017 COOL Chips 20 Cerebration for the 20th Anniversary of IEEE Symposium on Low-Power and High-Speed Chips

Hironori Kasahara

2017 President Elect, 2018 President

IEEE Computer Society

IEEE Fellow

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IEEE
computer
society

IEEE
Advancing Technology
for Humanity

April 20, 2017

Cool Chips Everywhere

➢ Low Power High Performance is the current most important issue in IoT, embedded systems including smart phones, automobiles, and medical apparatus, PCs, and servers including cloud servers and supercomputers.

➢ I, as the Computer Society president 2018, has been deeply impressed with the vision of founders of the Cool Chips and efforts of committees that have continued the Cool Chips for 20 years.

➢ I hope the Cool Chips will continue another 20 years as the IEEE leading Symposium.

IEEE Computer Society

H. Kasahara, IEEE COOL Chips, April 20, 2017

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Hironori Kasahara Voted 2017 IEEE Computer Society President-Elect

LOS ALAMITOS, Calif., 30 September 2016 — Hironori Kasahara, a Professor of Computer Science at Waseda University in Tokyo, and Director of the Advanced Multicore Research Institute, has been voted **IEEE Computer Society 2017 President-Elect.**

Kasahara is a former member of the IEEE-CS Board of Governors, has served as chair of the IEEE-CS Multicore STC and CS Japan Chapter, and board member of the IEEE Tokyo Section. Kasahara will serve as the 2018 IEEE CS President for a one-year term beginning 1 January 2018. Kasahara garnered 3,278 votes, compared with 2,804 votes cast for Hausi A. Müller, a Professor of Computer Science and Associate Dean of Research, Faculty of Engineering at University of Victoria, Canada, and a member of IEEE-CS Board of Governors.

The President oversees IEEE-CS programs and operations and is a nonvoting member of most IEEE-CS program boards and committees. The 2016 election had a 12.69% turnout, with 6,357 ballots cast. The turnout was higher than the 2015 election with and 12.68% turnout (6,239 ballots cast) and the 2014 election with a 12.66% turnout (6,728 ballots cast).

2016 IEEE Computer Society Election Results

Press Release | Ballot counts

Posted 29 September 2016

Hironori Kasahara selected 2017 President-Elect (2018 President)

Hironori Kasahara has served as a chair or member of 225 society and government committees, including a member of the CS Board of Governors; chair of CS Multicore STC and CS Japan chapter; associate editor of IEEE Transactions on Computers; vice PC chair of the 1996 ENIAC 50th Anniversary International Conference on Supercomputing; general chair of LCPC; PC member of SC, PACT, PPoPP, and ASPLOS; board member of IEEE Tokyo section; and member of the Earth Simulator committee.

He received a PhD in 1985 from Waseda University, Tokyo, joined its faculty in 1986, and has been a professor of computer science since 1997 and a director of the Advanced Multicore Research Institute since 2004. He was a visiting scholar at University of California, Berkeley, and the University of Illinois at Urbana–Champaign's Center for Supercomputing R&D.

Kasahara received the CS Golden Core Member Award, IFAC World Congress Young Author Prize, IPSJ Fellow and Sakai Special Research Award, and the Japanese Minister's Science and Technology Prize. He led Japanese national projects on parallelizing compilers and embedded multicores, and has presented 210 papers, 132 invited talks, and 27 patents. His research has appeared in 520 newspaper and Web articles.

H. Kasahara, IEEE COOL Chips, April 20, 2017

Past IEEE Computer Society Presidents

Chairs of the IRE Professional Group on Electronic Computers

1951-53	Morton M. Astrahan
1953-54	John H. Howard
1954-55	Harry Larson
1955-56	Jean H. Felker
1956-57	Jerre D. Noe
1957-58	Werner Buchholz
1958-59	Willis H. Ware
1959-60	Richard O. Endres
1960-62	Arnold A. Cohen
1962-64	Walter L. Anderson

Chairs of the AIEE Committee on Large-Scale Computing Devices

1946-49	Charles Concordia
1949-51	John Grist Brainerd
1951-53	Walter H. MacWilliams
1953-55	Frank J. Maginniss
1955-57	Edwin L. Harder
1957-59	Morris Rubinoff
1959-61	Ruben A. Imm
1961-63	Claude A. Kagan
1963-64	Gerhard L. Hollander

Chairs & Presidents of the IEEE Computer Society

1964-65	Keith Uncapher
1965-66	Richard I. Tanaka
1966-67	Samuel Levine
1968-69	Charles L. Hobbs
1970-71	Edward J. McCluskey
1972-73	Albert S. Hoagland
1974-75	Stephen S. Yau
1976	Dick B. Simmons
1977-78	Merlin G. Smith
1979-80	Tse-Yun Feng
1981	Richard E. Merwin
1982-83	Oscar N. Garcia
1984-85	Martha Sloan
1986-87	Roy L. Russo
1988	Edward A. Parrish
1989	Kenneth A. Anderson
1990	Helen M. Wood
1991	Duncan H. Lawrie
1992	Bruce D. Shriver
1993	James H. Aylor
1994	Laurel V. Kaleda
1995	Ronald G. Hoelzeman
1996	Mario R. Barbacci
1997	Barry W. Johnson
1998	Doris L. Carver
1999	Leonard L. Tripp
2000	Guylaine M. Pollock
2001	Benjamin W. Wah
2002	Willis K. King
2003	Stephen Diamond
2004	Carl K. Chang
2005	Gerald L. Engel
2006	Deborah M. Cooper
2007	Michael R. Williams
2008	Rangachar Kasturi
2009	Susan K. (Kathy) Land,
2010	James D. Isaak
2011	Sorel Reisman
2012	John W. Walz
2013	David Alan Grier
2014	Dejan S. Milojicic
2015	Thomas M. Conte
2016	Roger U. Fujii
2017	Jean-Luc Gaudiot
2018	**Hironori Kasahara**

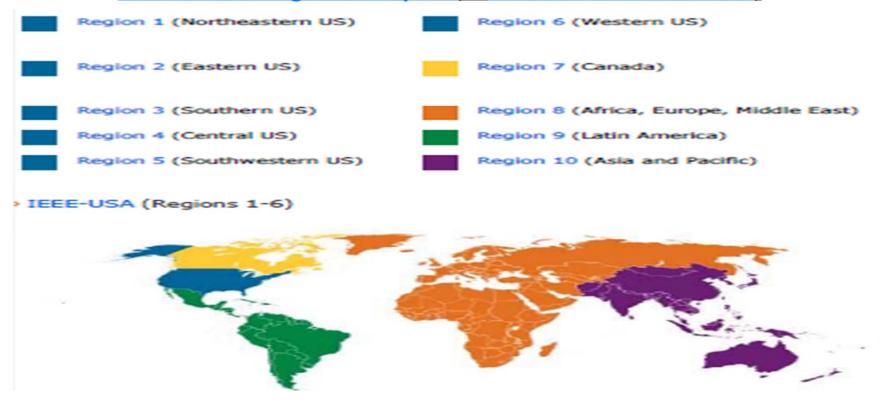H. Kasahara, IEEE COOL Chips, April 20, 2017

IEEE Computer Society

60,000+ members,   **volunteer-led organization,**
200 technical conferences, industry-oriented "**Rock Stars**",
17 scholarly journals and **13 magazines,** **awards program,**
Digital Library with more than 550,000 articles and papers ,
400 local and regional chapters,   **40 technical committees,**

Region 1 (Northeastern US)   Region 6 (Western US)

Region 2 (Eastern US)   Region 7 (Canada)

Region 3 (Southern US)   Region 8 (Africa, Europe, Middle East)

Region 4 (Central US)   Region 9 (Latin America)

Region 5 (Southwestern US)   Region 10 (Asia and Pacific)

> IEEE-USA (Regions 1-6)

IEEE Computer Society BoG (Board of Governors) Feb.1, 2017

H. Kasahara, IEEE COOL Chips, April 20, 2017

Toward 2018

1. Refining <u>content and services</u> to further improve the <u>satisfaction of CS members</u>;   Incl. Trans. & Conf. Collaboration

2. Considering an <u>incentive for volunteers</u> to further accelerate CS activities and promptly provide technical benefits for people around the globe;

3. Offering more <u>attractive services</u> for practitioners in <u>industry</u>;

4. Providing the <u>world's best educational content</u> and historical treasures for future generations, which only the CS can create with our pioneering researchers (for example, the Multicore Compiler Video Series found at www.computer.org/web/education/multicore-video-series);

5. Thinking about <u>sustainable membership fees</u> while considering the diversity of economic situations within the 10 regions;

6. <u>Cooperating</u> with <u>other IEEE societies and sister societies</u> in a timely and efficient manner;

7. Intelligibly introducing the latest computer-related technologies to <u>younger generations, including children</u>, so that they can realize their technological dreams.