

Nominee for IEEE Computer Society President-Elect (2016 President)

## Hironori Kasahara

**Election by Internet:** 

August 4 - 12:00 noon EDT, October 6, 2014

You will receive an e-mail from IEEE-CS for the voting procedure.

http://www.computer.org/portal/web/election/president-elect

Position statement

Computer products such as smart-phones, wearable devices, cloud servers, supercomputers, self-driving automobiles, and heavy particle beam cancer therapy systems have changed our lives. Today, computer-based systems can only be developed through collaboration. The success of IEEE's initiatives such as Cloud, Smart Grid, and Internet of Things, depend on collaborations. In my professional and volunteer life, I have strived towards collaboration and I believe that I can help the Society.

With my 33 years of membership and volunteer work, I have demonstrated the technical and management experience necessary to lead the Computer Society. My experience with publications and conferences will help me to provide leadership to the broadest array of members and nonmembers alike. My work has spanned the spectrum of academia, government, and industry on an international basis. Based on this, I will reach out around the world, to engage technology experts, and to promote CS to a wider audience.

If elected, I will exert my utmost effort to make the Computer Society more attractive to its members, other IEEE society members, practitioners and managers in industry and government. Also, I will try to give young people technological dreams.

The Computer Society's core competence includes the advanced technological seeds inside our society and practical needs in other IEEE societies. Therefore, constructing synergistic intersociety cooperative relations will be very important for the future.

To this end, further efforts will be required for intelligible and attractive information dissemination of our advanced technologies to non-professionals. The efforts will be also effective for the collaboration with industry and government.

Furthermore, I will try to start new activities to promote research in academia and development of value-added products in industry and to leave academic heritage for future generations, for example like the Multicore Compiler Video Course being prepared in the Multicore STC with top researchers representing this era.

I believe strongly the Computer Society has the ability to be more agile in creating modern products and services attractive to young and experienced members and potential members.

For more information, please visit www.kasahara.cs.waseda.ac.jp/kasahara.html.en.

## **Biography**

Hironori Kasahara has served as a chair or a member of 220 society and government committees including as member of the IEEE CS Board of Governors, chair of IEEE CS Multicore STC, IEEE CS Japan Chapter, IPSJ SIG on Computer Architecture, METI computer roadmap committee, and member of IEEE CS Nomination committee, the Board of IEEE Tokyo Section, IEEE Japan Council Long-Term Strategy Committee, MEXT Information Science and Technology Committee, and MEXT Earth Simulator and K supercomputer committees.

He received a PhD in Electrical Engineering from Waseda University, Tokyo, in 1985 where he has been a professor of computer science since 1997 and a director of the Advanced Multicore Research Institute. He was a visiting scholar at University of California, Berkeley, and University of Illinois at Urbana-Champaign's Center for Supercomputing R&D.

He received an IEEE CS Golden Core Member Award, an IFAC World Congress Young Author Prize, an IPSJ Special Research Award, a Science and Technology Prize from the Minister of Education, Science and Technology.

He led four Japanese national projects on parallelizing compilers, multicores, and green computing supported by METI/NEDO. His works were presented as 195 papers, 120 invited talks, 26 patents, and 450 newspaper and Web articles.