



IEEE Computer Society Board of Governor Nominee

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Position statement

The importance of computer technology has been increasing in lives due to the influence of technologies like mobile phones, digital TVs, games, PCs, servers, automobiles, robotics, and supercomputers. However, most IT-related societies, including our IEEE Computer Society, have been facing problems like declining subscriptions to periodicals and fewer members from industry. In addition, interest in computer technology among youth, including high school students, seems to be declining. If elected, I will do my best to make the Computer Society more attractive to the Computer Society members and also people in industry and the younger generation, based on my experience collaborating with industry for research and development of low power multicore processors, supercomputers and compilers and with the media and governments for science and technology challenge contests for high school students. The activity will include support of the IEEE Computer Society Certification for Software Development Professionals (CSDP) and Certified Software Development Associate (CSDA) for students. Please visit www.kasahara.cs.waseda.ac.jp/kasahara.en.html.

Biography

Hironori Kasahara has served as a chair of the IEEE Computer Society Japan Chapter, a board member of the IEEE Tokyo Section, a member of the IEEE Japan Council Long-Term Strategy Committee, and as publication chair or program committee of nine IEEE conferences, including the International Conference on Parallel and Distributed Systems and Supercomputing. He has chaired the Information Processing Society of Japan (IPSJ) SIG on Computer Architecture, served on the *IPSJ Journal* editorial board as head of the hardware working group, and served as vice program chair of the ENIAC 50th anniversary celebration at ICS.

In 1985, Kasahara received a PhD in electrical engineering from Waseda University, Tokyo, where he has been a professor of computer science since 1997, and a director of the Advanced Chip Multiprocessor Research Institute. Kasahara was a visiting scholar at the University of California, Berkeley and the University of Illinois at Urbana-Champaign's Center for Supercomputing Research & Development. Kasahara received the Young Author Prize at the International Federation of Automatic Control World Congress, an IPSJ Sakai Memorial Special Research Award, and a Semiconductor Technology Academic Research Center Industry-Academia Cooperative Research Award. He led Japanese national projects on parallelizing compilers and multicores in METI/NEDO.