Multicore STC Lecture Course Video Making

Multicore Compiler

http://www.computer.org/portal/web/STC-Multicore/home

September 6 and 7, Thomas M. Siebel Center for Computer Science, 2405 Siebel Center
201 North Goodwin Avenue, Urbana, IL 61801-2302 Univ. of Illinois at Urbana-Champaign

Chair: Prof. David Padua, IEEE CS Multicore STC Steering Committee Chair, UIUC

Saturday, September 6
8:30-8:45 Opening Address Hironori Kasahara, IEEE Multicore STC Chair, Waseda U
9:00-10:00 Overview David Kuck, IEEE CS Multicore STC Advisory Committee Chair, Intel
10:30-11:30 Dependences and Dependence Analysis Utpal Banerjee, UC Irvine
12:00-1:30 Lunch
1:30-2:30 The Polyhedral Model Paul Feautrier, Ecole Normale Supérieure de Lyon
3:00-4:00 Vectorization P. Sadayappan, Ohio State U
4:30-5:30 Parallelization David Padua, UIUC

Sunday, September 7
8:00-9:00 Autoparallelization for GPUs Wen-Mei Hwu, UIUC
9:30-10:30 Instruction Level Parallelization Alexandru Nicolau, UC Irvine
11:00-12:00 Multigrain Parallelization and Power Reduction Hironori Kasahara, Waseda U
12:30-1:30 Lunch
1:30-2:30 Dynamic Parallelization Rudolf Eigenmann, Purdue U, NSF
3:00-3:30 Vectorization/Parallelization in the Intel Compiler Peng Tu, Intel
4:00-4:30 Vectorization/Parallelization in the IBM Compiler Yaoqing Gao, IBM
5:00-6:30 Panel All participants

<Lecture videos by Prof. Tom Conte, Georgia Tech. and Prof. Monica Lam, Stanford U. will be taken on another day.>